Figure 1

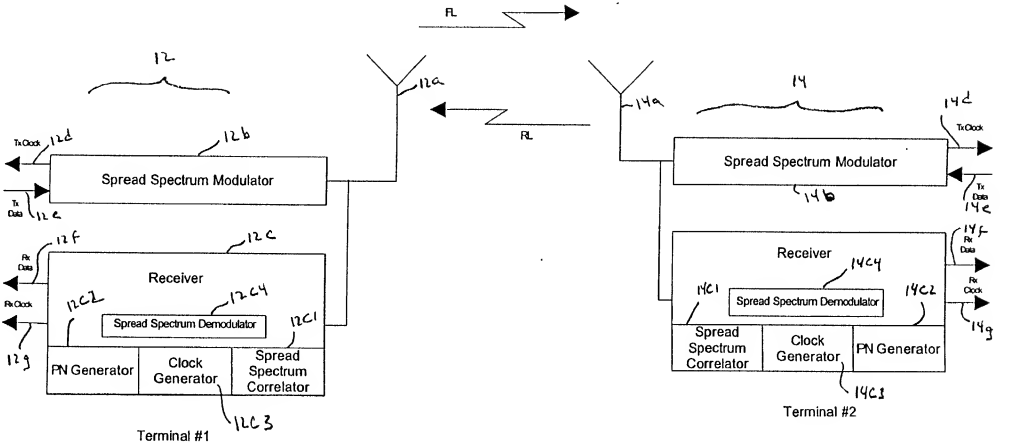

FL
12
12a
FL
14a
14
14d
Tx Clock
12d
12b
Spread Spectrum Modulator
Tx Data
12e
Spread Spectrum Modulator
14b
Tx Clock
Tx Data
14e
12f
12c
Rx Data
Receiver
14f
Rx Data
14c4
12c2
12c4
14c1
Spread Spectrum Demodulator
14c2
Rx Clock
12c1
Rx Clock
14g
12g
Spread Spectrum Correlator
Clock Generator
PN Generator
PN Generator
Clock Generator
Spread Spectrum Correlator
12c3
14c3
Terminal #1
Terminal #2
10

Figure 3A

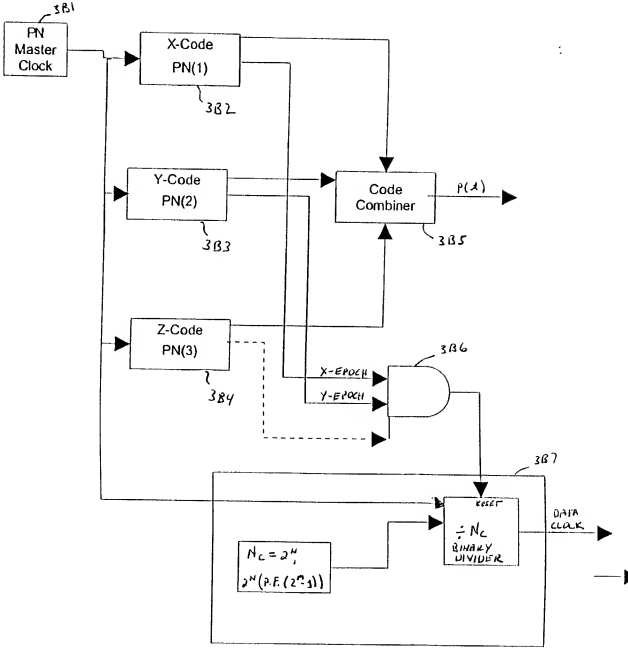


Figure 3C

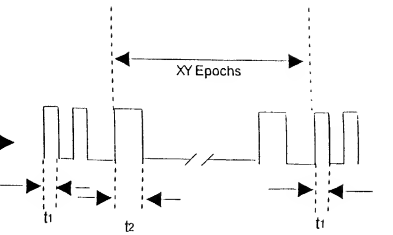


Figure 2A

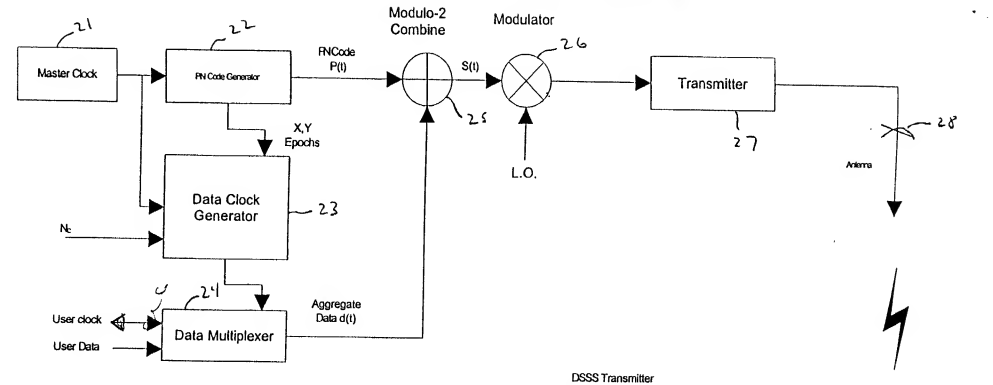


Figure 2B

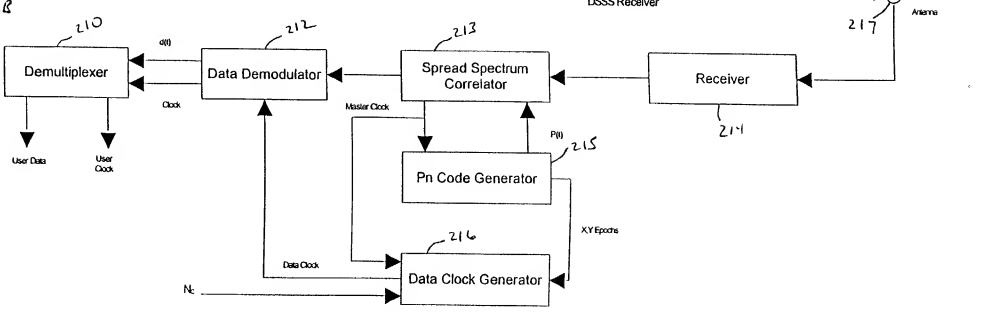
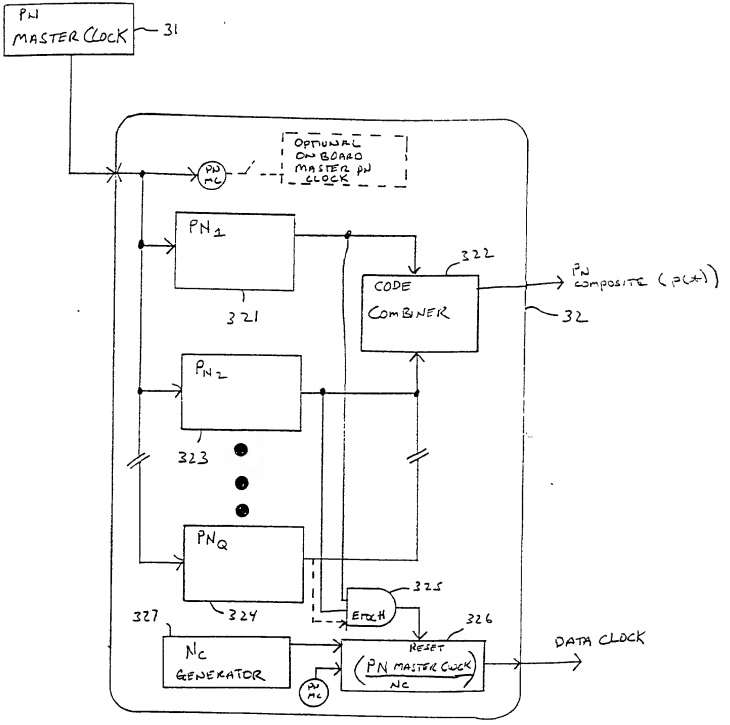
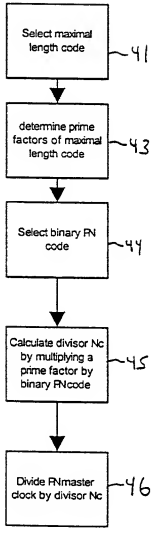


Fig. 3B



2002510.022602

Figure 4.



10035410.022602

Figure 5
Divisor Nc Formulation Table

n	2^n	2^n * 3	2^n * 3^2	2^n * 5	2^n * 7	2^n * 13
0	1	3	9	5	7	13
1	2	6	18	10	14	26
2	4	12	36	20	28	52
3	8	24	72	40	56	104
4	16	48	144	80	112	208
5	32	96	288	160	224	416
6	64	192	576	320	448	832
7	128	384	1152	640	896	1664
8	256	768	2304	1280	1792	3328
9	512	1536	4608	2560	3584	6656
10	1024	3072	9216	5120	7168	13312
11	2048	6144	18432	10240	14336	26624
12	4096	12288	36864	20480	28672	53248

Figure 6
Selected Divisor Nc and Step Size

Nc	Step	Nc	Step	Nc	Step	Nc	Step
1	1	24	4	160	32	1152	128
2	1	28	4	192	32	1280	256
3	1	32	4	224	32	1536	256
4	1	36	4	256	32	1792	256
5	1	40	4	288	32	2048	256
6	1	48	8	320	64		
7	1	56	8	384	64		
8	1	64	8	448	64		
9	1	72	8	512	64		
10	2	80	8	576	64		
12	2	96	16	640	128		
14	2	112	16	768	128		
16	2	128	16	896	128		
18	2	144	16	1024	128		
20	2						

20020220 01958001

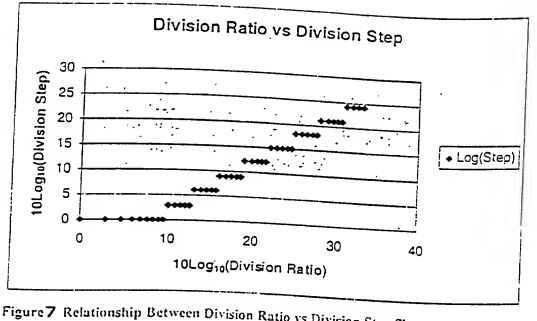


Figure 7 Relationship Between Division Ratio vs Division Step Size